

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

**Confirmation No. 3422**

Yutaka ITO et. al.

Date: March 29, 2007

Serial No.: 10/699,223

Group Art Unit: 2187

Filed: October 30, 2003

Examiner: James R. GOLDEN

For: DRAM WITH SUPER SELF-REFRESH AND ERROR CORRECTION FOR  
EXTENDED PERIOD BETWEEN REFRESH OPERATIONS (As Amended)

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VIA EFS WEB

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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**AMENDMENT AFTER ALLOWANCE UNDER RULE 312**

Sir:

The Notice of Allowance dated incorrectly lists the title of the application as:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

The correct title should read:

DRAM WITH SUPER SELF-REFRESH AND ERROR CORRECTION FOR EXTENDED  
PERIOD BETWEEN REFRESH OPERATIONS

as modified in response to an objection by the Examiner in an Office Action dated January 10, 2006 by Amendment dated May 1, 2006, a copy of which, showing receipt by the Patent Office, is attached for the Examiner's convenience, and submission of which was acknowledged in the Final Office Action dated August 1, 2006, also attached.

The title should be listed as such on the Notice of Allowance and the issuing patent.

Respectfully submitted,

THIS CORRESPONDENCE IS BEING  
SUBMITTED ELECTRONICALLY  
THROUGH THE UNITED STATES  
PATENT AND TRADEMARK OFFICE  
EFS FILING SYSTEM  
ON MARCH 29, 2007



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